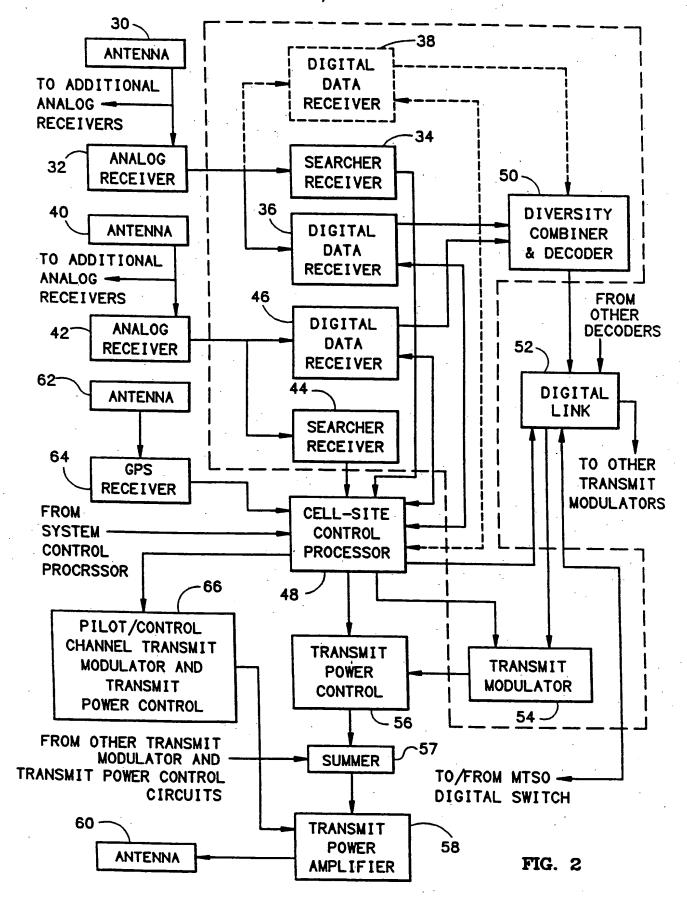
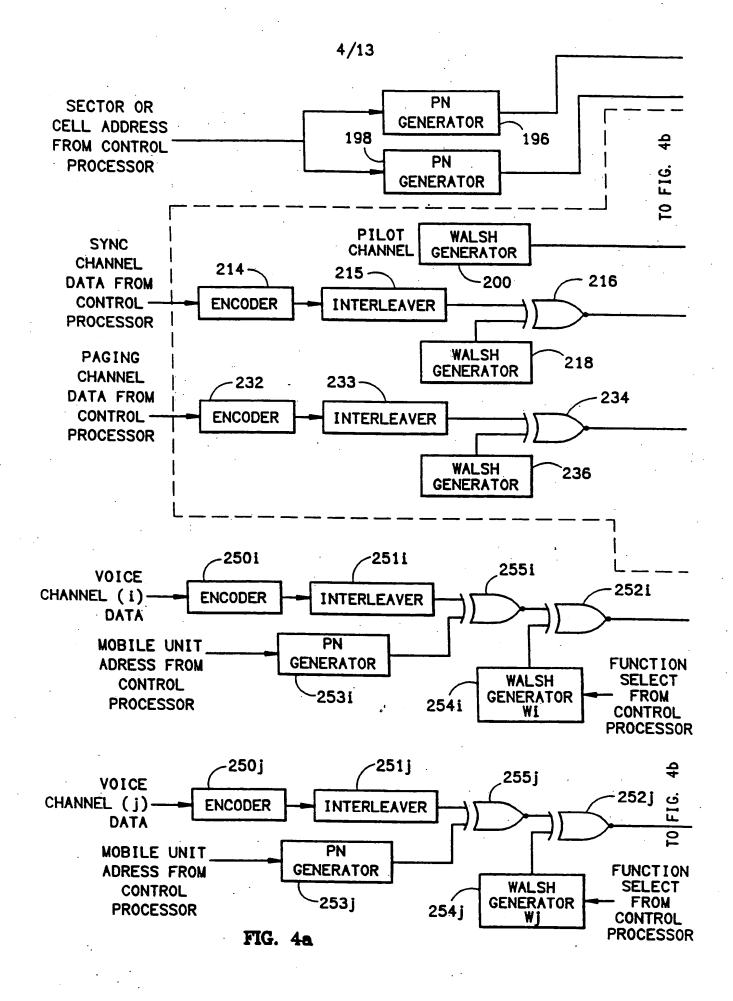
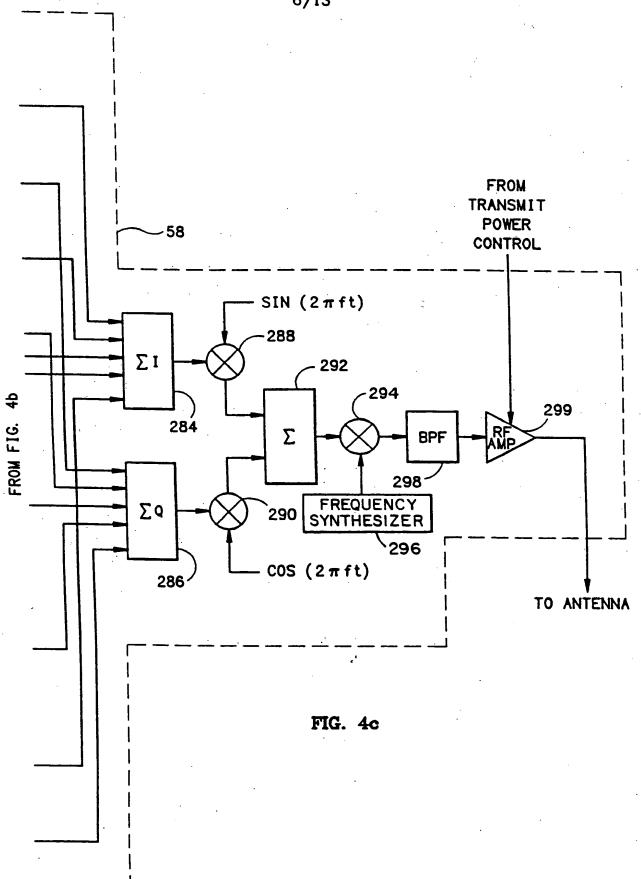


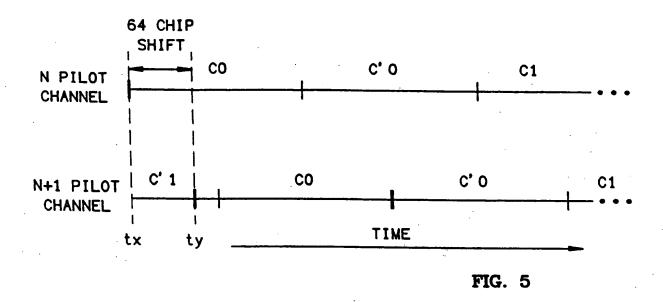
FIG. 1

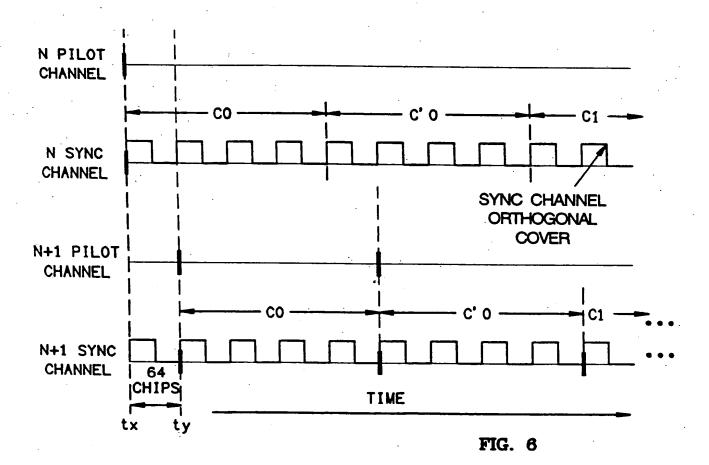


CONTROL PROCESSOR









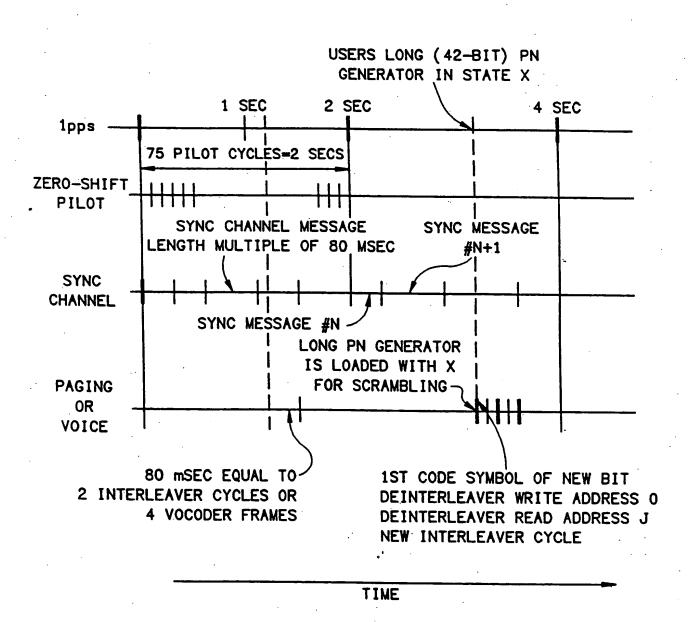


FIG. 7

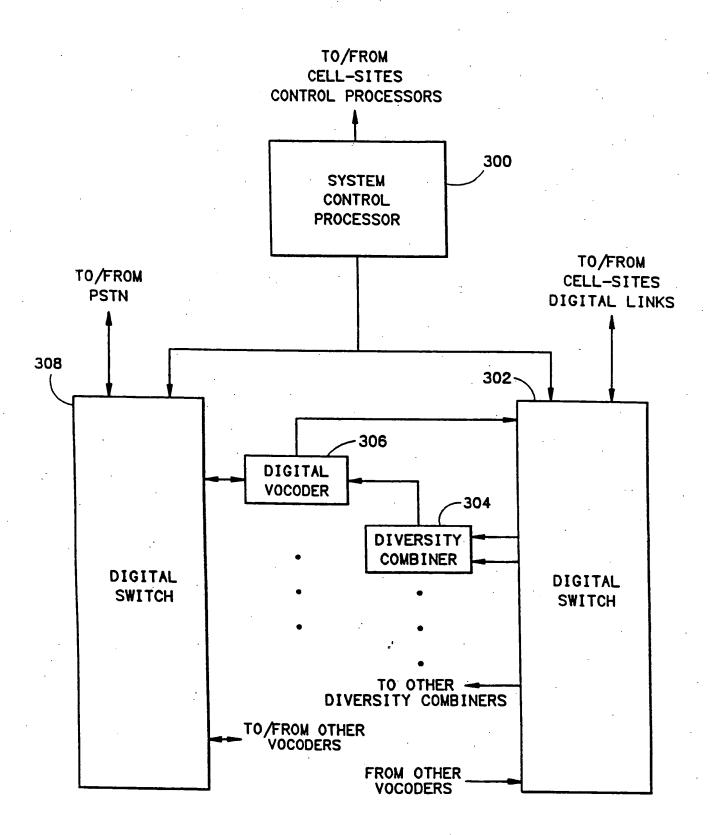
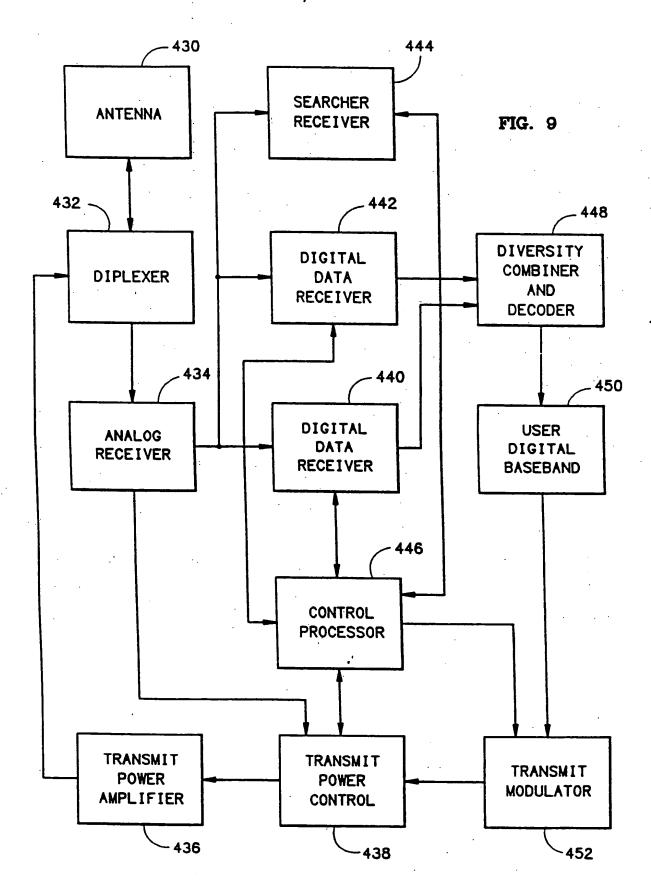
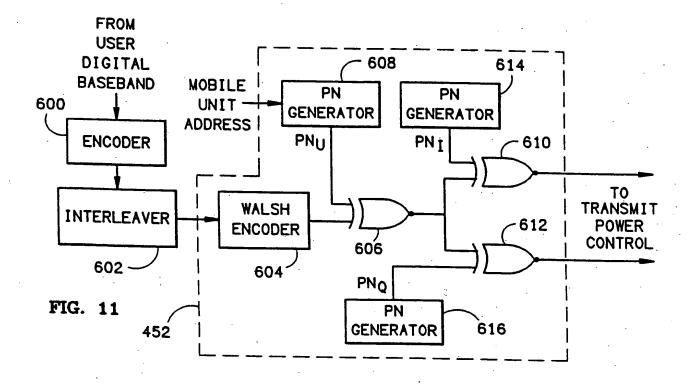


FIG. 8





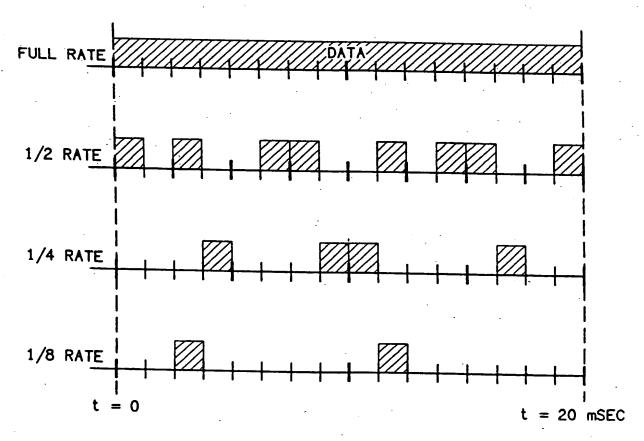


FIG. 12

